

Course Code: E047

## RISC-V Fundamentals (LFD210) Course + Exam

### COURSE INFORMATION

Sessions  
—Duration  
—Level  
—Assessment  
NA

#### VENUE

12 Woodlands Square #07-85/86/87 Woods Square Tower 1, Singapore 737715. 5 mins walk from Woodlands (NS9) MRT station.

The venue is disabled-friendly.

### WHAT'S THIS COURSE ABOUT

- Chapter 1. Course Introduction
- Chapter 2. RISC-V Overview
- Chapter 3. Introduction to RISC-V Instruction Set
- Chapter 4. Understanding Instruction Formats and Pseudoinstructions
- Chapter 5. The Modularity of RISC-V as an ISA
- Chapter 6. Privileges and the Memory Model
- Chapter 7. Assembly Language for RISC-V
- Chapter 8. Writing and Debugging RISC-V Assembly Code
- Chapter 9. High-Level Languages for RISC-V: C Programming
- Chapter 10. The GNU C Compiler for RISC-V
- Chapter 11. Clang and LLVM for...

### COURSE FEE

Full Fee S\$300.00 Before GST

GST S\$27.00 9% of fee

Total Payable S\$327.00 Including GST

## CERTIFICATION

- **Certificate of Completion from Tertiary Infotech** - Upon meeting at least 75% attendance and passing the assessment(s), participants will receive a Certificate of Completion from Tertiary Infotech.

## REGISTRATION

<https://www.tertiarycourses.com.sg/risc-v-fundamentals-lfd210.html>



SCAN TO REGISTER

## SUPPORT

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