



Course Code: C716

Mastering Semiconductor Process Fundamentals for CMOS Process Integration

COURSE INFORMATION

Sessions
2 days

Duration
15 hrs

Level
Beginner

Assessment
NA

VENUE

12 Woodlands Square #07-85/86/87 Woods Square Tower 1, Singapore 737715. 5 mins walk from Woodlands (NS9) MRT station.

The venue is disabled-friendly.

WHAT'S THIS COURSE ABOUT

Topic 1 CMOS Process Modules and Process Flow

* CMOS process modules

Photolithography and Mask

Etch

Clean

Ion Implantation

Diffusion and RTP

Film

CMP

CMOS process flows

Topic 2: CMOS Process Integration and Performance

CMOS process integration and sort yield

CMOS device and reliability performances

Statistical process control

Packaging technologies

Overview of future IC technologies - Chiplet, FINFET and GAAFET

COURSE FEE

Full Fee S\$600.00 Before GST

GST S\$54.00 9% of fee

Total Payable S\$654.00 Including GST

CERTIFICATION

- **Certificate of Completion from Tertiary Infotech** - Upon meeting at least 75% attendance and passing the assessment(s), participants will receive a Certificate of Completion from Tertiary Infotech.

REGISTRATION

<https://www.tertiarycourses.com.sg/mastering-semiconductor-process-fundamentals-for-cmos-process-integration.html>



SCAN TO REGISTER

SUPPORT

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