



Course Code: C421

FPGA Designer Training

COURSE INFORMATION

Sessions
2 days

Duration
15 hrs

Level
Intermediate

Assessment
NA

VENUE

12 Woodlands Square #07-85/86/87 Woods Square Tower 1, Singapore 737715. 5 mins walk from Woodlands (NS9) MRT station.

The venue is disabled-friendly.

WHAT'S THIS COURSE ABOUT

Topic 1: Introduction to FPGA

Cyclone/Stratix device Architecture

Introduction to Quartus II

Creating Project

Using Editor & Design Entry

Topic 2: Analysis and Elaboration

I/O Assignment

Configure voltage for I/O

I/O Assignment Analysis

Synthesis

Netlist Viewer

Topic 3: Constraints

Importance of Constraints in Design

Clock frequency

Asynchronous & Synchronous Design

False Path/Multicycle path

Topic 4: Debugging Tools

Power Analysis

SignalTap II embedded...

COURSE FEE

Full Fee S\$600.00 Before GST

GST S\$54.00 9% of fee

Total Payable S\$654.00 Including GST

CERTIFICATION

- **Certificate of Completion from Tertiary Infotech** - Upon meeting at least 75% attendance and passing the assessment(s), participants will receive a Certificate of Completion from Tertiary Infotech.

REGISTRATION

<https://www.tertiarycourses.com.sg/fpga-designer-training.html>



SCAN TO REGISTER

SUPPORT

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